



SSCE5V021N7

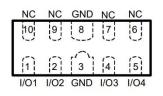
Ultra Low Capacitance Array for ESD Protection

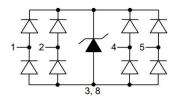
Description

The SSCE5V021N7 is an ultra low capacitance TVS array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines.

The SSCE5V021N7 has an ultra-low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) with ±25kV air and ±25kV contact discharge. It is assembled into a 10-pin 2.5x1.0x0.5mm lead-free DFN package. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines such as USB 3.0 and HDMI. The small size, ultra-low capacitance and high ESD surge protection make SSCE5V021N7 an ideal choice to protect HDMI, MDDI, USB 3.0 and other high speed ports.

PIN configuration





Top view



Marking

• Applications

- ♦ DVI & HDMI Port Protection
- ♦ Serial and Parallel Ports
- ♦ Projection TV
- ♦ Notebooks, Desktops, Server
- ♦ USB 1.1/2.0/3.0/3.1/OTG
- ♦ HDMI 1.3, HDMI 1.4

• Mechanical data

- ♦ Lead finish:100% matte Sn(Tin)
- ♦ Mounting position: Any
- ♦ Qualified max reflow temperature:260°C
- ♦ Device meets MSL 1 requirements
- ♦ Pure tin plating: $7 \sim 17$ um
- ♦ Pin flatness:≤3mil

• Feature

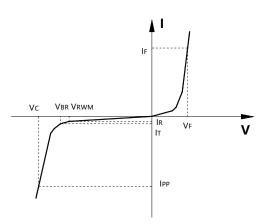
- ♦ 70W peak pulse power ($t_P = 8/20\mu s$)
- ♦ DFN2510-10L Package
- ♦ Working voltage: 5V
- ♦ Low clamping voltage
- ♦ Low capacitance
- ♦ RoHS compliant transient protection for high speed data lines to IEC61000-4-2(ESD)±25kV(air),±25kV(contact)



SSCE5V021N7

• Electronic Parameter

| Symbol | Parameter | | |
|------------------|--|--|--|
| V _{RWM} | Peak Reverse Working Voltage | | |
| IR | Reverse Leakage Current @ V _{RWM} | | |
| V _{BR} | Breakdown Voltage @ I _T | | |
| IT | Test Current | | |
| I _{PP} | Maximum Reverse Peak Pulse Current | | |
| Vc | Clamping Voltage @ I _{PP} | | |
| Ррр | Peak Pulse Power | | |
| CJ | Junction Capacitance | | |



• Absolute maximum rating @TA=25°C

| Parameter | Symbol | Value | Unit | |
|--------------------------------------|------------------|----------|------|--|
| Peak Pulse Power (8/20µs) | P _{PP} | 70 | W | |
| Peak Pulse Current (8/20µs) | I _{PP} | 5 | А | |
| ESD Rating per IEC61000-4-2: Contact | | 25 | | |
| Air | V _{ESD} | 25 | KV | |
| Storage Temperature | T _{STG} | -55/+150 | °C | |
| Operating Temperature | TJ | -55/+125 | °C | |

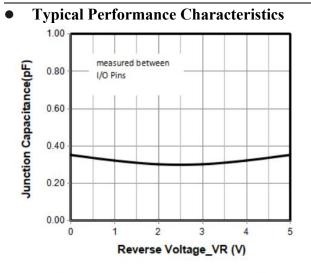
• Electrical Characteristics @TA=25°C

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
|------------------------------|------------------|--------------------------------|------|------|------|------|
| Peak Reverse Working Voltage | V _{RWM} | Any I/O to GND | | | 5 | V |
| Breakdown Voltage | V _{BR} | $I_T = 1 m A$ | 6 | | | V |
| | | Any I/O to GND | | | | |
| Reverse Leakage Current | I _R | $V_{RWM} = 5.0V$ | | | 1 | μΑ |
| Diode Forward Voltage | V _F | I _F = 15mA | | 0.85 | 1.2 | V |
| Clamping Voltage | Vc | $I_{PP} = 1A, tP = 8/20 \mu s$ | | 9.5 | | V |
| Clamping Voltage | Vc | $I_{PP}=5A, tP = 8/20\mu s$ | | | 14 | V |
| Junction Capacitance | CJ | $V_R = 0V, f = 1MHz,$ | | 0.3 | 0.4 | pF |
| | | between I/O pins | | | | |
| | | VR = 0V, f = 1MHz, | | 0.6 | 0.8 | pF |
| | | any I/O pin to GND | | | | |

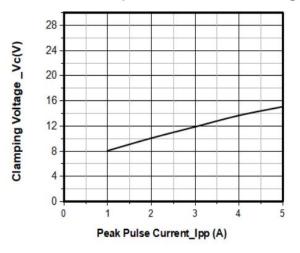
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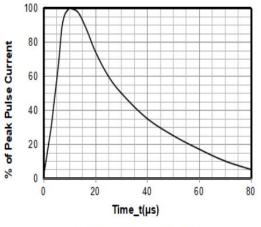
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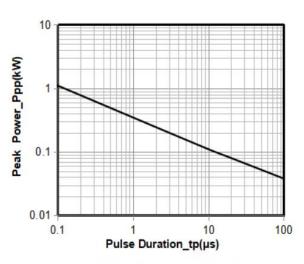
Junction Capacitance vs. Reverse Voltage



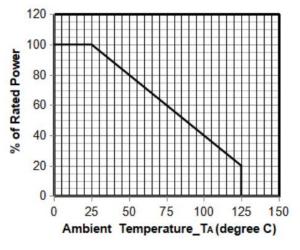
Clamping Voltage vs. Peak Pulse Current



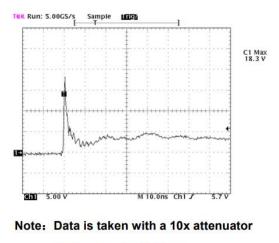
8 X 20µs Pulse Waveform







Power Derating Curve



ESD Clamping Voltage

8 kV Contact per IEC61000-4-2



• Package Information

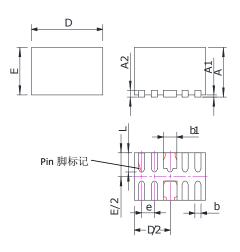
Ordering Information

| Dev | ice | Package | Qty per Reel | Reel Size |
|--------|-------|-------------|--------------|-----------|
| SSCE5V | 021N7 | DFN2510-10L | 3000 | 7 Inch |

Mechanical Data

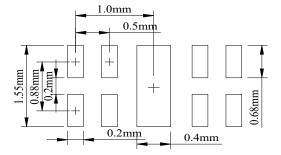
Case:DFN2510

Case Material: Molded Plastic. UL Flammability



| DIM | Millimeters | | |
|-----|-------------|------|--|
| DIM | Min | Max | |
| А | 0.45 | 0.65 | |
| A1 | 0.05REF | | |
| A2 | 0.15REF | | |
| b | 0.15 | 0.25 | |
| b1 | 0.30 | 0.50 | |
| D | 2.424 2.576 | | |
| E | 0.924 1.076 | | |
| е | 0.50REF | | |
| L | 0.30 | 0.45 | |

Recommended Pad outline





History Version

| V1.0 | Product datasheet | 2021-08-02 |
|------|---|------------|
| V1.1 | 1.Add marking Icon | 2022-04-26 |
| | 2. Update typical performance characteristics | |

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